

## 1000-3000MHz/50Watt/Module

*Model Number: KB1030M47A*

The model KB1030M47A is a multi-octave high power amplifier operating between 1000 MHz and 3000 MHz and offering a wide dynamic Range with 50 Watts typical saturated power. The employment of gallium nitride (GaN) and chip-and-wire technology in manufacturing ensures this module state-of-the-art power performance with excellent power-to-volume ratio. It is ideal for multi-octave broadband high power RF linear applications.

**FEATURES:**

- Suitable for RF, L&S linear applications
- Small size and light weight
- Unconditional stability and ruggedness
- Built-in control, and protection circuits

**ELECTRICAL SPECIFICATIONS @ +28.0VDC, 25°C, 50Ω**

Parameter	Symbol	Min	Typ	Max	Units
Operating Frequency	BW	1000		3000	MHz
RF Output Power	Pout	50			Watt
Power Gain	Gp		47		dB
Power Gain Flatness	Δ Gp		±1.5		dB
Input Return Loss	S11			-10	dB
Harmonics @20W	H		-15		dBc
Spurious Signals	Spur		-60		dBc
Switching Speed	TON/OFF		2	5	μS
In/Output Impedance	Impedance		50		Ω
Operating Voltage	VDC	24	28	32	Volt
Current Consumption @ POUT =50W	Ibd		8		Amp

**MECHANICAL SPECIFICATIONS**

Parameter	Value	Units	Notes
Dimensions	150x90x25 [5.9x3.55x0.98]	mm [inch]	Maximum
Weight	1.2 [2.6]	kg [lbs]	Maximum
RF Connectors Input	SMA, Female		
RF Connectors Output	SMA, Female		
DC Interface Connector	D-Sub 9-Pin, Male		
Cooling	External Heatsink Required (Not Supplied)		

**ENVIRONMENTAL CHARACTERISTICS (Design to Meet)**

Parameter	Minimum	Typical	Maximum	Units	Notes
Operating Temperature	-20		60	°C	
Non-operating Temperature	-30		75	°C	Storage
Relative Humidity (non-condensing)			95	%	

**Absolute Maximum Rating**

Input RF drive level without damage	+10 dBm (Max)
Load VSWR @ POUT =30W	∞ @ all load phase & amplitude for duration of 1 minutes; 3:1 @ all load phase & amplitude continuous

*Focus on Your Application*

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*Specifications subject to change without notice*

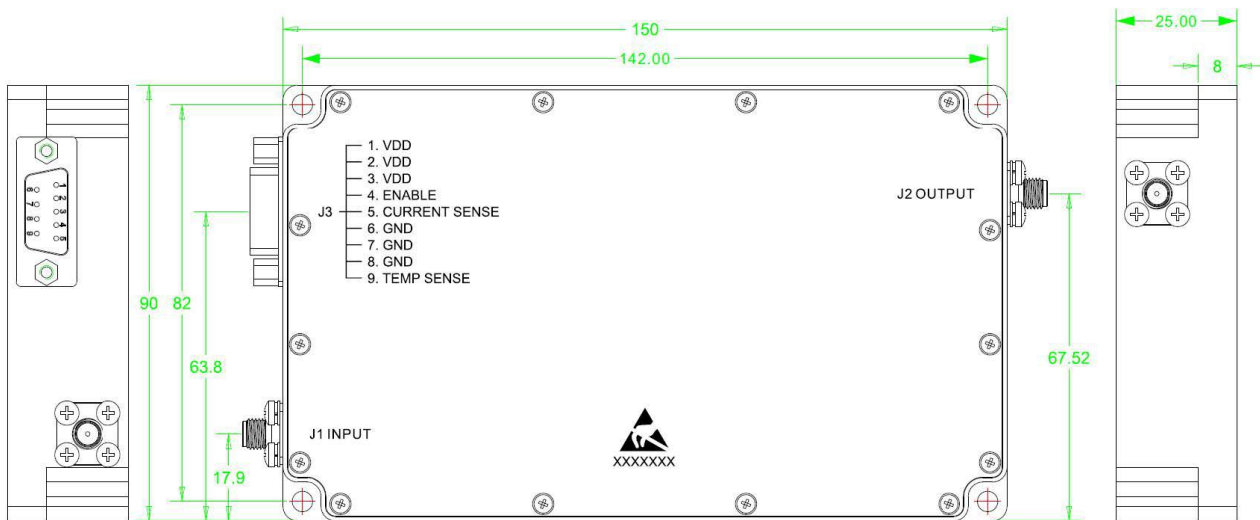
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DC INTERFACE CONNECTOR

Pin #	Description	Specifications
1,2,3	VDD	28V <sub>DC</sub>
4,	ENABLE	Amplifier Enable: TTL Logic High (3.3V) (Internally Pulled-Low)
5	CURRENT SENSE	Analog voltage relative to I <sub>DD</sub> @ 100mV per Ampere
6,7,8	GND	Ground
9	TEMP SENSE	Analog voltage relative to Module's Temperature @ 10 mV/°C

OUTLINE DRAWING (All dimensions in mm[inch])



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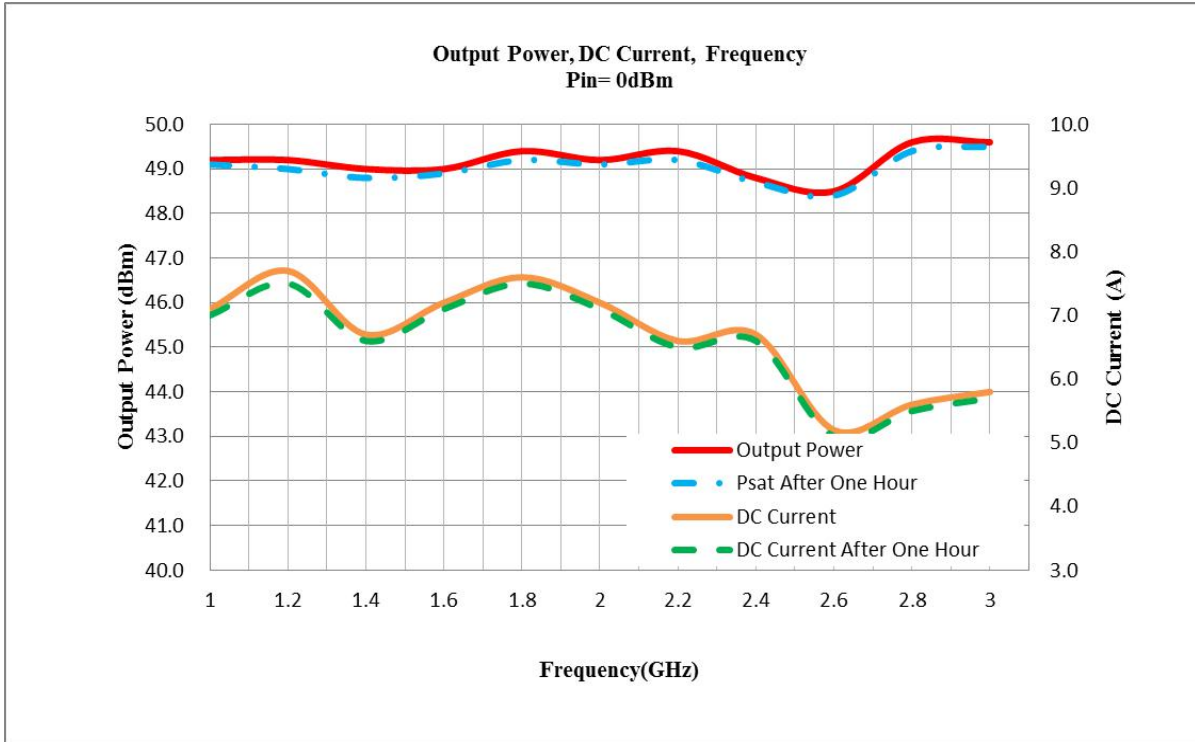
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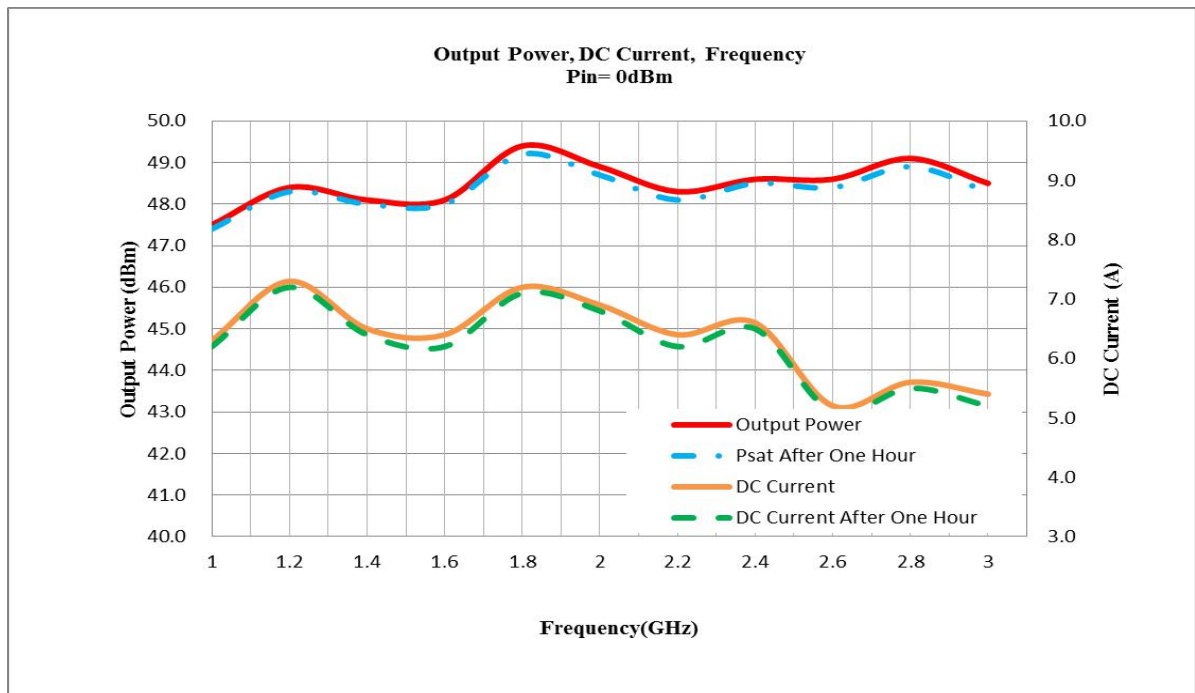
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TYPICAL PERFORMANCE PLOTS (for reference only)

Graph1: Output Power (Low temp. -20±3 °C)



Graph2: Output Power(High temp. +60±3 °C)



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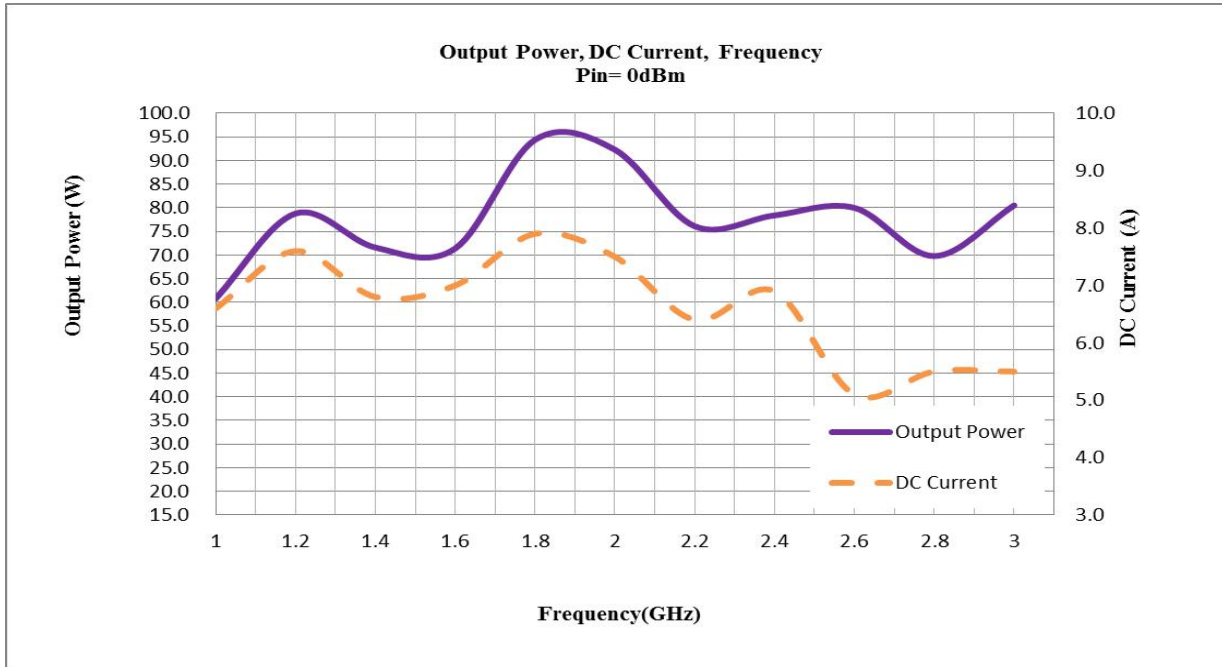
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Graph3: Output Power (Normal temp.+25±3°C)



Power Gain:



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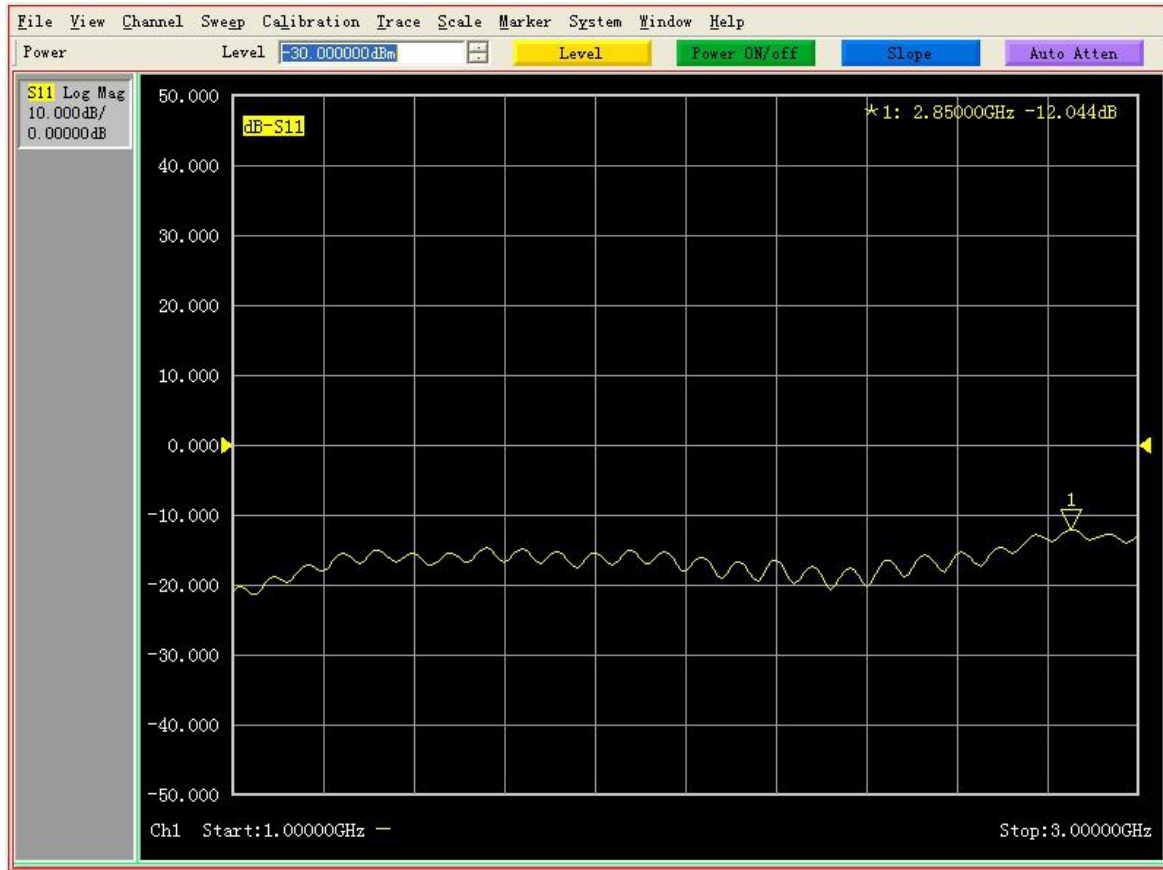
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Input Return Loss:



Note: Adequate heatsink required.

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